next communication to applicant.

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

High-Voltage Transistor With A Multi-Layer Conduction Region

Application Number:

09/961229

Confirmation Number:

4375

First Named Applicant: Vladimir Rumennik

Attorney Docket Number: 03692.P007XD4

Art Unit:

2811

Examiner:

Shouxiang Hu

Search string:

(6633065 or 6639277).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
Spo	1	6633065	2003-10-14	Rumennik et al.	B2		
90	2	6639277	2003-10-28	Rumennik et al.	B2		

Remarks

Note: Remarks are not for responding to an office action.

This information disclosure statement is being submitted concurrent with the filing of a Request for Continued Examination in the above-referenced application.

Signature

Examiner Name	Date
Shouwongflu	1/20/04